## FPGA Acceleration of Apache Spark ML on the Cloud, Instantly

ゆう三 inacce

Dr. Chris Kachris CEO, co-founder www.inaccel.com ...or How to speedup your Spark ML applications with the same cost with the same code

### Why acceleration

> 91% of Spark users for Big Data analytics care about Performance



#### **FPGAs in the news**

News & Analysis Microsoft Eyes Expanding FPGA Role

Network chips not keeping pace



Intel Delivers Xeon Scalable Processor 6138P with Arria 10 GX 1150 FPGA Ratchets Up FPGAs in Data Center by Kevin Morris

Nimbix Teams with Xilinx to Expand FPGA-Based Workload Acceleration in the Cloud

Baidu Deploys Xilinx FPGAs in New Public Cloud Acceleration Services

#### Intel, Alibaba Demo FPGAs in Cloud

March 10, 2017 by George Leopold

May 29, 2018

Xilinx Powers Huawei FPGA Accelerated Cloud Server

#### **Available Platforms**



Specialized chips/FPGA + High Performance - low flexibility

|                                   |  | -  |  |
|-----------------------------------|--|--|--|
| Next Gen<br>Programmable<br>Logic |  | Application<br>Processors<br>Real-Time<br>Processors |  |
|                                   |  |  |  |

#### **Available Platforms**



Application Processors

Real-Time Processors

GPIO & Mem I/F

### **Apache Spark**

- Spark is the most widely used framework for Data Analytics
- Develop hardware components as IP cores for widely used applications
  - >> Spark
    - Logistic regression
    - Recommendation
    - K-means
    - Linear regression
    - PageRank
    - Graph computing



#### Market size

> The data center accelerator market is expected to reach USD 21.19 billion by 2023 from USD 2.84 billion by 2018, at a CAGR of 49.47% from 2018 to 2023.

> The market for FPGA is expected to grow at the highest CAGR during the forecast period owing to the increasing adoption of FPGAs for the acceleration of enterprise workloads.

[Source: Data Center Accelerator Market by Processor Type (CPU, GPU, FPGA, ASIC)- Global Forecast to 2023, Research and Markets]









### helps companies speedup their applications

by providing ready-to-use accelerators-as-a-service in the cloud



#### **Acceleration for machine learning**

inaccel offers Accelerators-as-a-Service for Apache Spark in the cloud (e.g. Amazon AWS f1) using FPGAs





ADVANCED ANALYTICS USERS (MLLIB)



### Hardware acceleration

FPGA handles computeintensive, deeply pipelined, hardware-accelerated operations









#### CPU handles the rest

Source: amazon, Inc.

#### Accelerators for Spark ML in Amazon AWS in 3 steps



### **Cloud Marketplace: available now**







Scalable to worldwide market



**First** to provide accelerators for Spark

**IP cores available in Amazon AWS** 

### Logistic Regression K-mean clustering

#### Recommendation Engines (ALS)







Gradient Descent IP block for faster training of machine learning algorithms. K-means is one of the simplest unsupervised learning algorithms that solve the well known clustering problem. Alternative-Least-Square IP core for the acceleration of recommendation engines based on collaborative filtering.

Available in Amazon AWS marketplace for free trial: <u>www.inaccel.com</u>

### Communication with Host in Amazon AWS f1.x2 and f1.x16

Global Memory



FPGA

Accelerators for logistic regression/kmeans

### **Docker-based implementation for easy integration**



- Inaccel's FPGA manager docker container comprises both an FPGA manager to schedule, orchestrate, and monitor the execution of the accelerated applications but also the required FPGA runtime system.
- > The dockerized runtime system detects the FPGA platform (aws F1) and manages the interaction/communication with the FPGA (i.e., loading the accelerator, transferring input data and results), making it transparent to the application.
- > Docker swarm, Kubernetes, naïve execution

#### **Cluster mode**

Driver (sparkContext) > Cluster Worker Worker Worker mode Executor Executor Executor inaccel Docker Runtime inaccel inaccel inaccel & FPGA Manager **IP** Library FPGA accelerated FPGA Infrastructure FPGA FPGA f1.x2 f1.x2 f1.x2

#### **Demo on Amazon AWS**



Intel 36 cores Xeon on Amazon AWS c4.8xlarge \$1.592/hour

8 cores + inaccel in Amazon AWS FPGA f1.2xlarge \$1.65/hour + inaccel

### **Speedup comparison**

> Up to 10x speedup compared to 32 cores based on f1.x2





#### > Up to 12x speedup compared to 64 cores on f1.x16



### **Speedup comparison**

# > 3x Speedup compared to r4> 2x lower OpEx



#### **Performance evaluation**



#### **Cost reduction**

> Up to 3x lower cost to train your ML model



### Try for free on Amazon AWS





#### Single node version

 Single-node Machine learning accelerators for Amazon
f1.x2large instances providing APIs for C/C++, Java, Python and Scala for easy integration

#### Single node ML suite

#### **Distributed version for Apache Spark**

> Machine learning accelerators for Apache Spark providing all the required APIs and libraries for the seamless integration in distributed systems

#### **Distributed node ML suite**

### **InAccel unique Advantages**



#### **Compatible with Amazon AWS**

All accelerators are compatible with the Amazon AWS F1 instances. AWS compatibility allows easy and fast deployment of the accelerators and seamless integration with your current AWS applications.



#### Seamless integration with your code

InAccel provides all the required APIs for the seamless integration of the accelerators without any modifications on your original code.



#### Acceleration of your code

Accelerators from InAccel provide up to 2x-10x speedup compared to contemporary processors in typical servers.



# Adaptable. Intelligent.

